



501.37959CC2

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Makoto YOSHIDA et al.
Serial No.: 10/653,889
Filed: September 4, 2003
For: **SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND
MANUFACTURING METHOD OF MINIATURIZATION OF
MEMORY CELLS WITH REDUCED BIT LINES (As Amended)**
Group: 2814
Examiner: Hoai V. Pham

*Entered with
RCE of 9/23/05*

DMK 9/29/05

AMENDMENT AND SUBMISSION UNDER 37 CFR §1.114

**Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450**

September 23, 2005

Sir:

In accordance with the provisions of 37 CFR §1.114, and in response to the Office Action dated March 23, 2005, the period of response for which extension of time is requested in the attached Petition for Extension of Time, please amend the above-identified application as follows.

Amendments to the Claims;

Remarks are included following the Amendments; and

An Appendix with Sketches illustrating features of the invention follows the Remarks.